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Substitute for form 1449A/PTO			Complete If Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			Application Number	R53(b) CONT. of S.N. 10/631,793	
			Filing Date	March 30, 2004	
			First Named Inventor	TAKIKAWA, KUMIKO	
			Art Unit	2811	
			Examiner Name	T. DICKEY	
Sheet	1	of	2	Attorney Docket Number	501.38451CC3

U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
TJ		US- 3,974,450		08-10-1976	Cunningham, Jr.	
TJ		US- 4,851,796		07-25-1989	Hendriks	
TJ		US- 5,880,601		03-09-1999	Kanazawa et al	
TJ		US- 5,903,827		05-11-1999	Kennan et al	
TJ		US- 6,522,895		02-18-2003	A. Montalvo	
TJ		US- 6,658,237		12-02-2003	Rozenblit et al	
		US-				
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FOREIGN PATENT DOCUMENTS							
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)					
TJ		EP 0823790 A2 and A3		02-01-2003	Nokia Corporation		
TJ		WO 95/02277		19-01-1995	Wireless Access, Inc.		
TJ		EP 0886384 A2		23-12-1998	Lucent Technologies, Inc.		

Examiner Signature		Date Considered	8/3/04
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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete If Known			
		Application Number	R53(b) CONT. of S.N. 10/631,793		
		Filing Date	March 30, 2004		
		First Named Inventor	TAKIKAWA, KUMIKO		
		Art Unit	2811		
Sheet	2	of	2	Examiner Name	T. DICKEY
				Attorney Docket Number	501.38451CC3

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
Tb		SHIN, Y.I.S., "MAINTAIN SIGNAL INTEGRITY AT HIGH DIGITAL SPEEDS", Electronic Design, 14 May 1992, Penton Publishing, Cleveland, OH [USA], Vol. 40, No. 10, ppgs. 77-78, 80, 82, 84, 86, 88, 89 and 91.	
Tb		TURINSKY, G., "CHIPTRAEGER FÜR HOCHGESCHWINDIGKEITS-IS", Veb Verlag Technik, Berlin, DE, Vol. 41, No. 1., 1992, ppgs. 25-26, [Germany]	
Tb		FONG, K.L., "DUAL-BAND HIGH-LINEARITY VARIABLE-GAIN LOW-NOISE AMPLIFIERS FOR WIRELESS APPLICATIONS", ISSCC 1999, pp. 224-225, p.463 (TP13.3, 24 pgs.).	
Tb		STEYAERT, Michiel et al, "A Single-Chip CMOS Transceiver for DCS-1800 Wireless Communications", ISSCC 1998, pp. 48-49, p. 411 (TP3.3, 23 pgs.).	

Examiner Signature		Date Considered	8/31/04
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